

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: S. OSHIMA, et al

Serial No.: Not yet assigned

Filed: January 13, 2004

For: METHOD FOR MONITORING FAULT OF OPERATING SYSTEM
AND APPLICATION PROGRAM

Group: 2184

Examiner: J. Lohn

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98**

MS DD

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 13, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

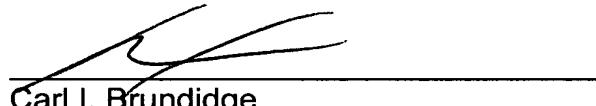
"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicant(s) are submitting herewith a copy of Form PTO-1449 which list documents cited in parent application(s) Serial No. 09/642,769, filed August 22, 2000.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (500.38928CX1) please credit any excess fees to such deposit account.

Respectfully submitted,



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FORM PTO-1449 U.S. Department of
Commerce (Rev. 4/92) Patent and Trademark
Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

SERIAL NO.

500.38928CX1

Not yet assignedAPPLICANT
S. OSHIMA, et alFILING DATE
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2184

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		5	8	7	5	4	8	4							
		2	0	0	2	5	3	7	1	2/99	Neuhard et al	711	167		
2 0 0 1										9/01	Sato et al	717	4		
		6	3	1	4	5	0	1	B 1	11/01	Gulick et al	711	153		
2 0 0 2										8/02	Oshima et al	714	55		
4	8	0	9	2	8	0				2/89	Shonaka	714	55		
6	4	4	6	2	2	5	B 1			9/02	Robzman et al	714	55		
2 0 0 1										8/01	Sekiguchi et al	709	319		

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
		1	0	2	2	2	3	8	8					YES	NO
		1	0	2	2	2	3	8	8	8/98	Japan	G06F001	20		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		T. Nanya, "Fault Tolerant Computer", published by Ohm-Sha, February 5, 1991, pp. 44-51 and 143-149.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.